

FIGURE 1

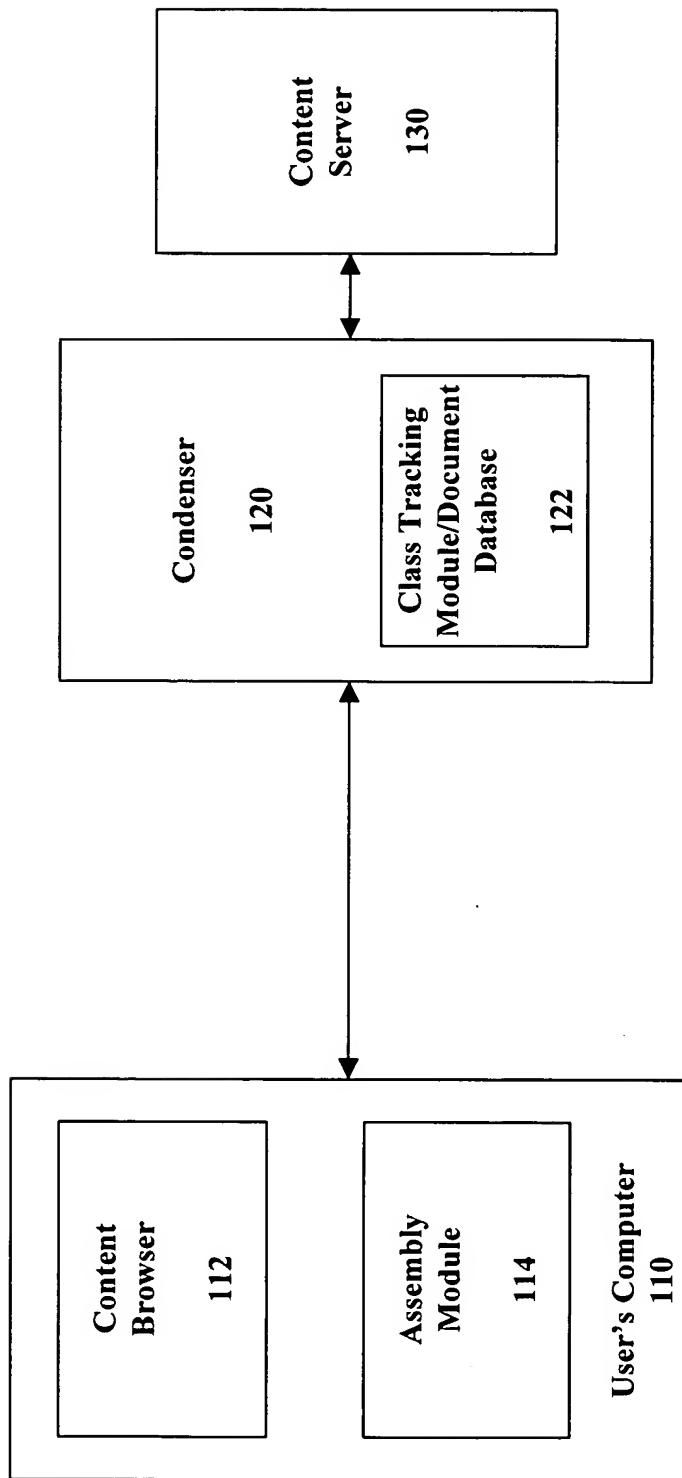


FIGURE 1

FIGURE 2
ROUTING SCHEMES

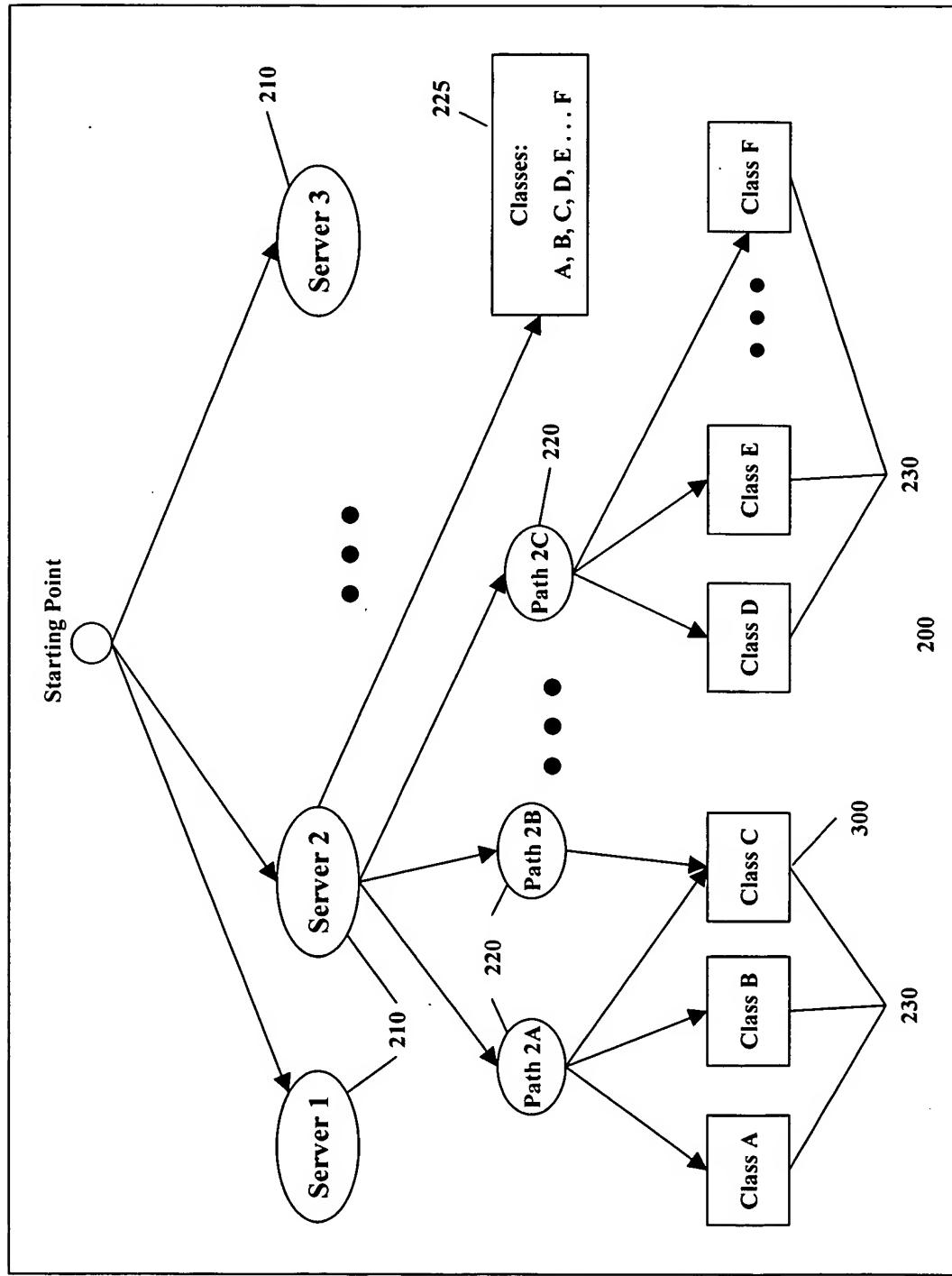


FIGURE 2

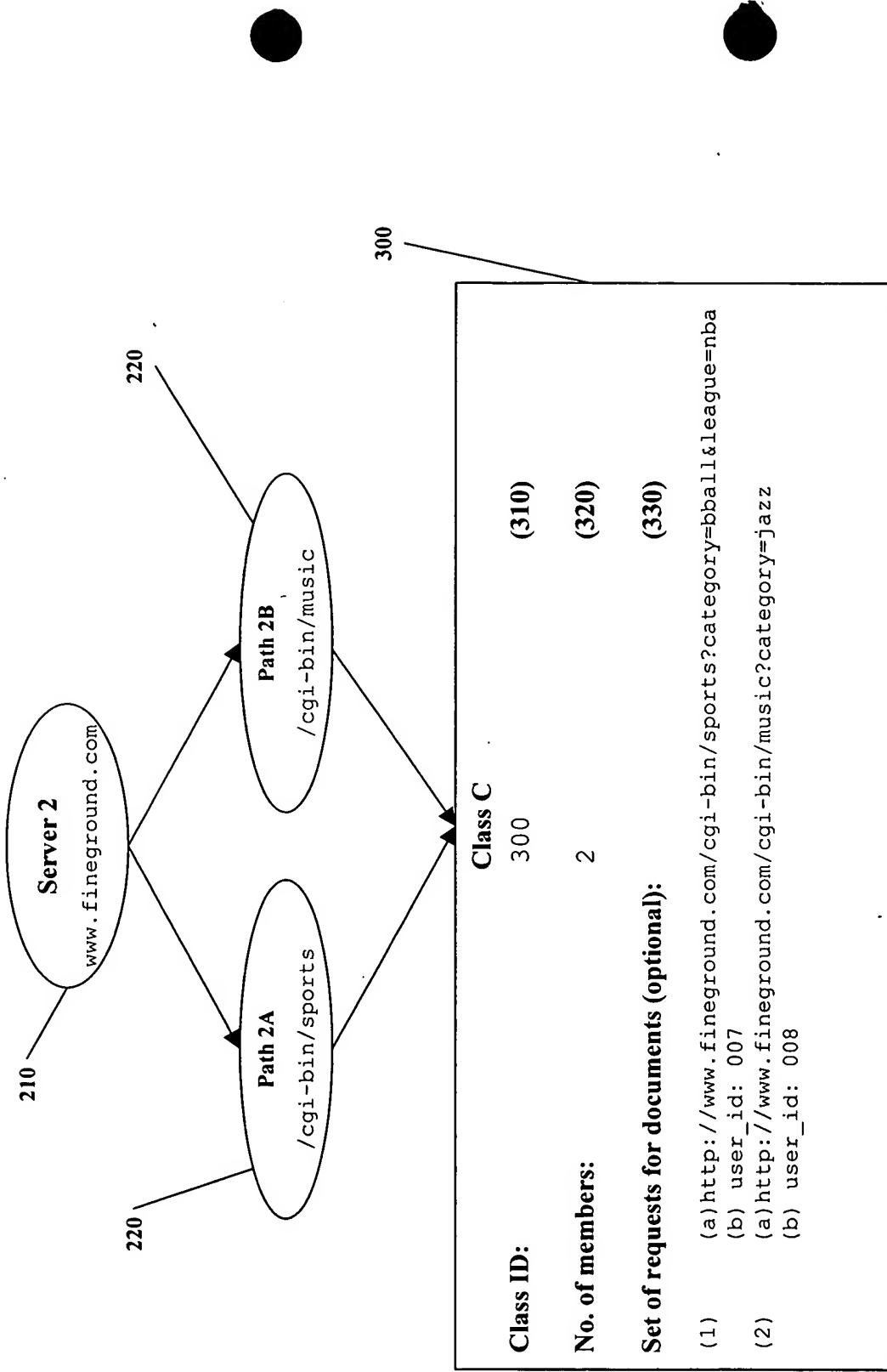


FIGURE 3

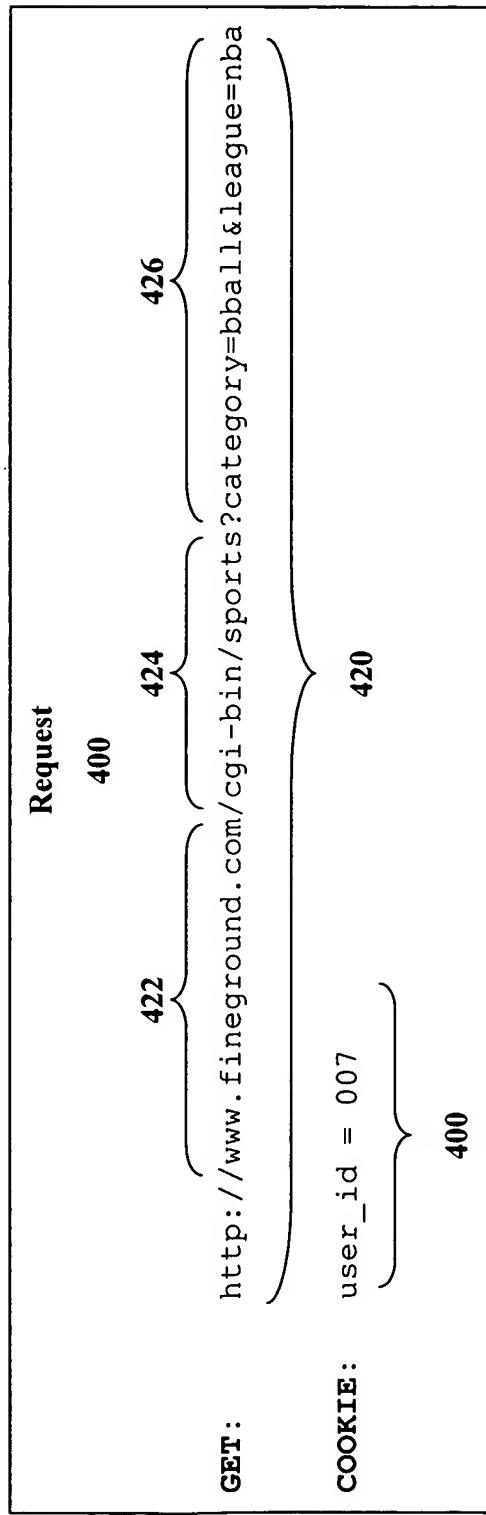


FIGURE 4

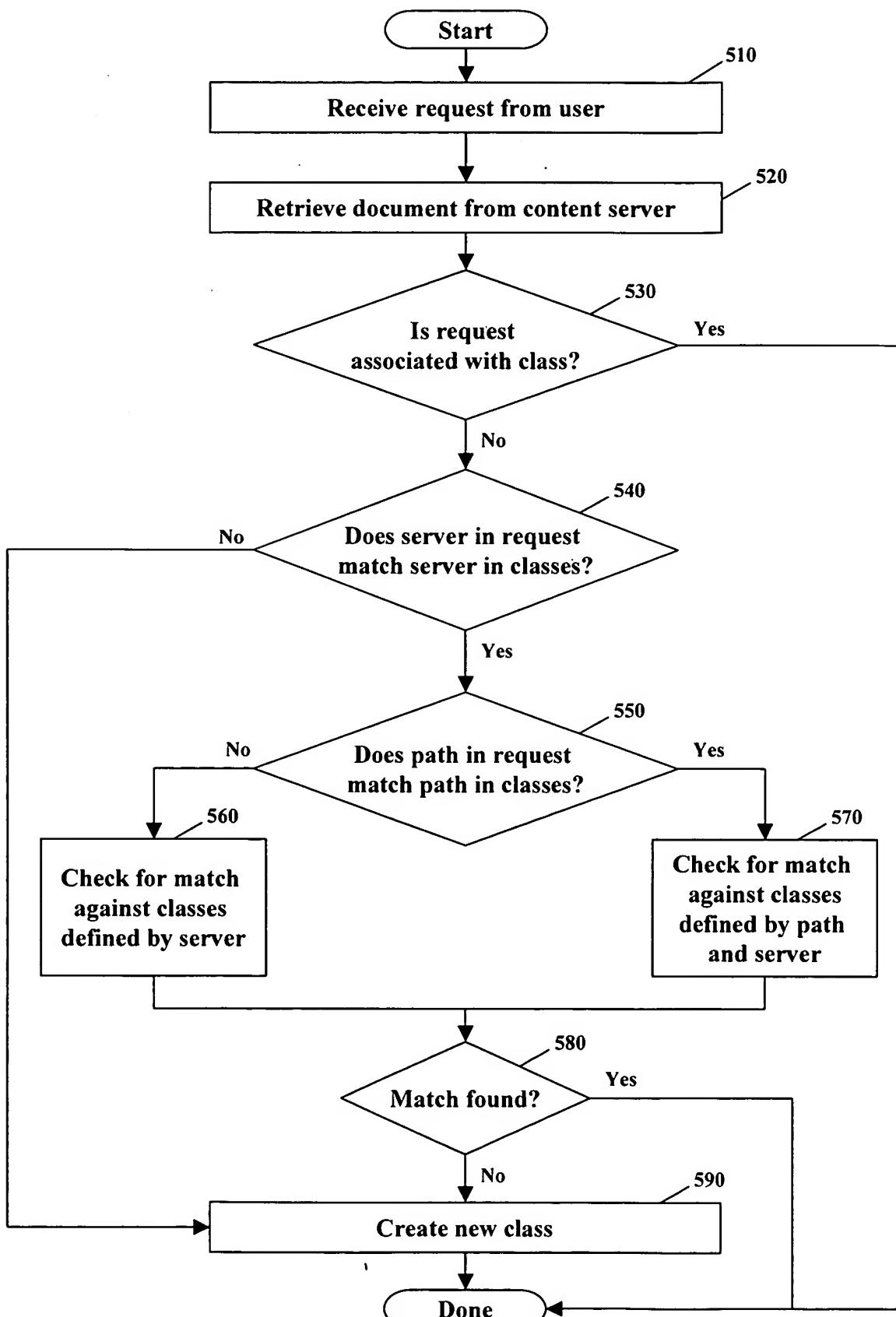


FIGURE 5

00043002102204

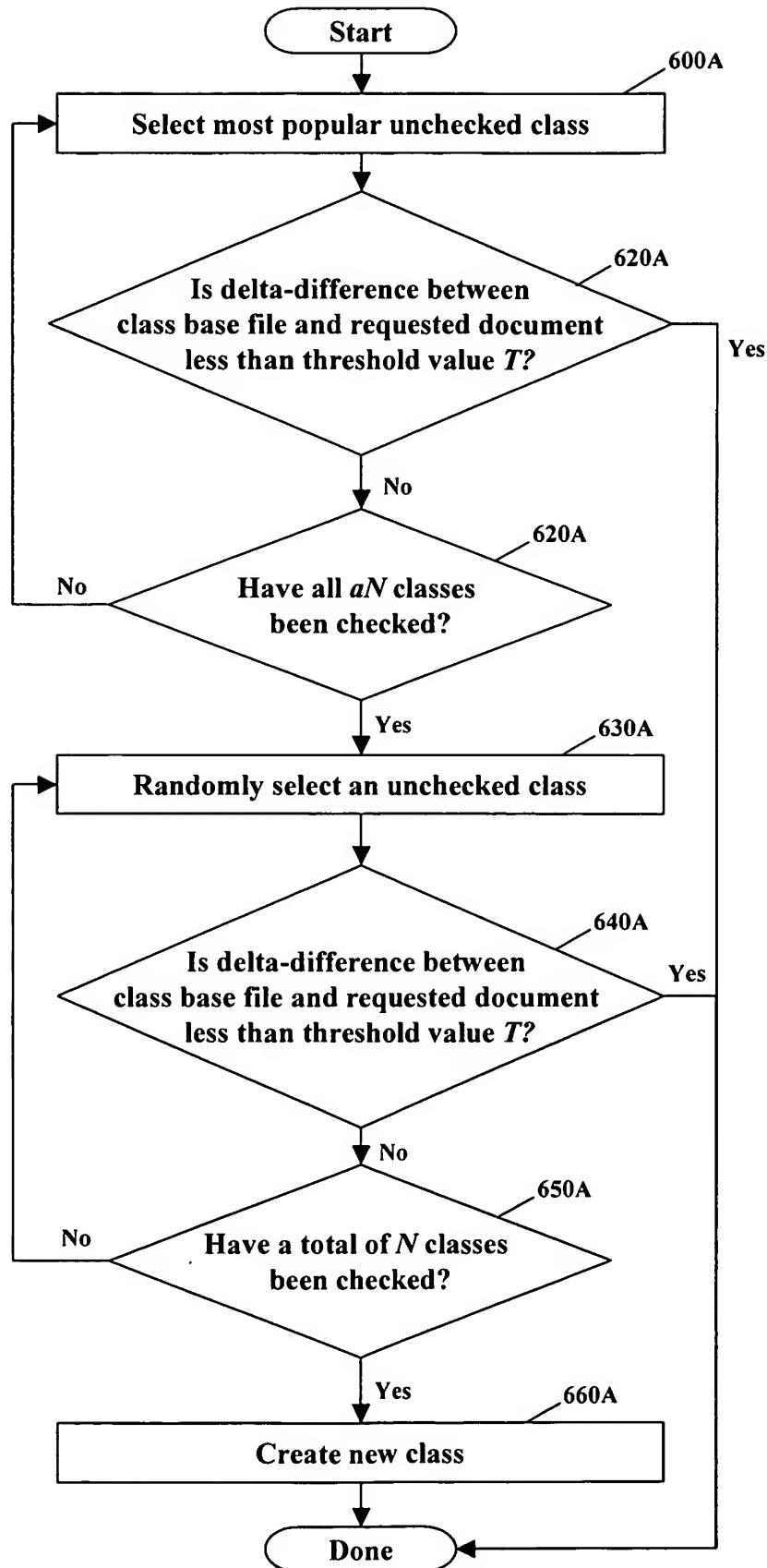


FIGURE 6A

200 201 202 203 204 205 206 207 208 209

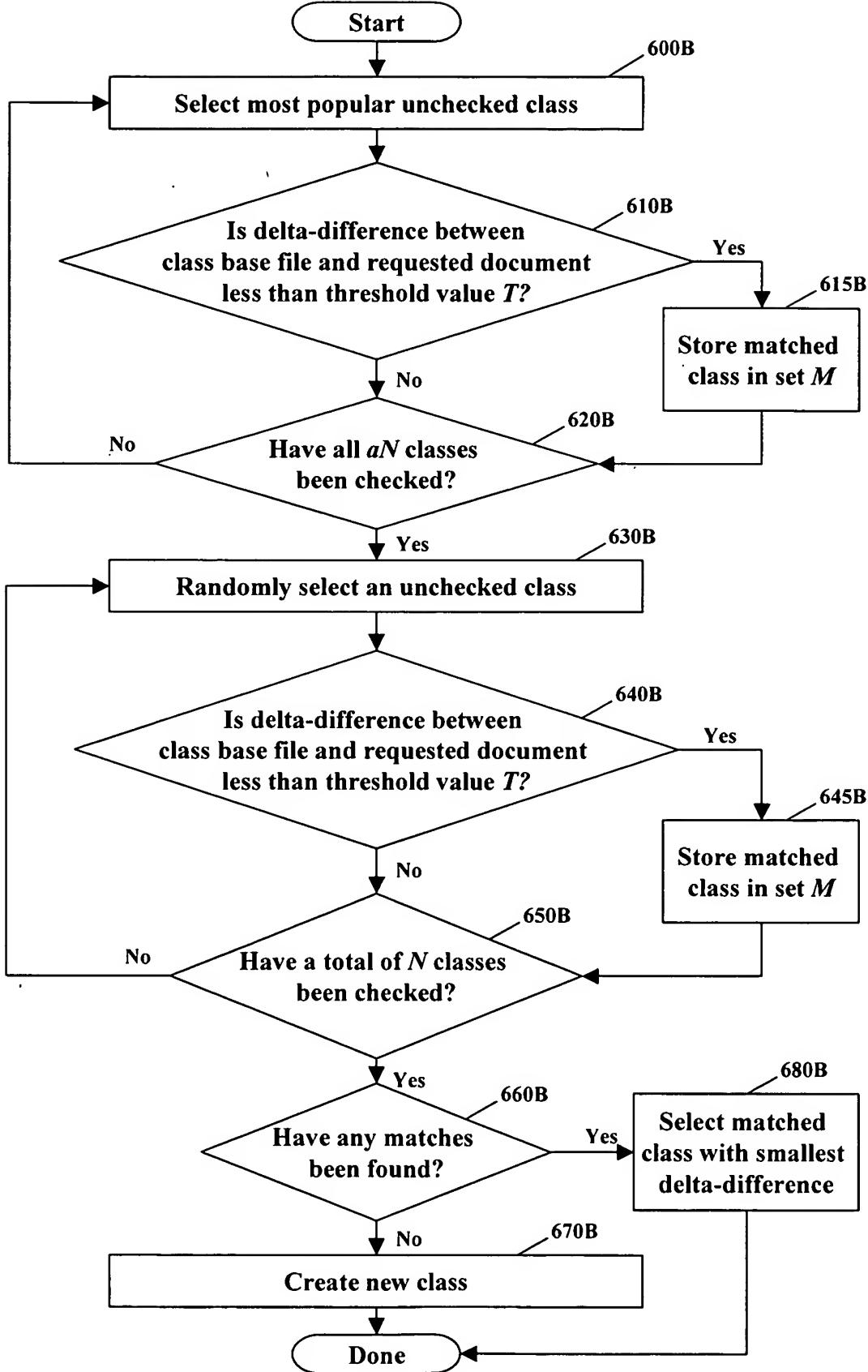


FIGURE 6B

0
0
0
0
0
0
0
0
0
0
0
0

	Sample 1	Sample 2	Sample 3	Sample K
Sample 1	-	d_{21}	d_{31}	d_{K1}
Sample 2	d_{12}	-	d_{32}	d_{K2}
Sample 3	d_{13}	d_{23}	-	d_{K3}
<hr/>				
Sample K	d_{1K}	d_{2K}	d_{3K}	-
<hr/>				
Sums	Σd_{1i}	Σd_{2i}	Σd_{3i}	Σd_{Ki}

700

FIGURE 7

800
810
820
830
835
840
850
860
870
875
880
890

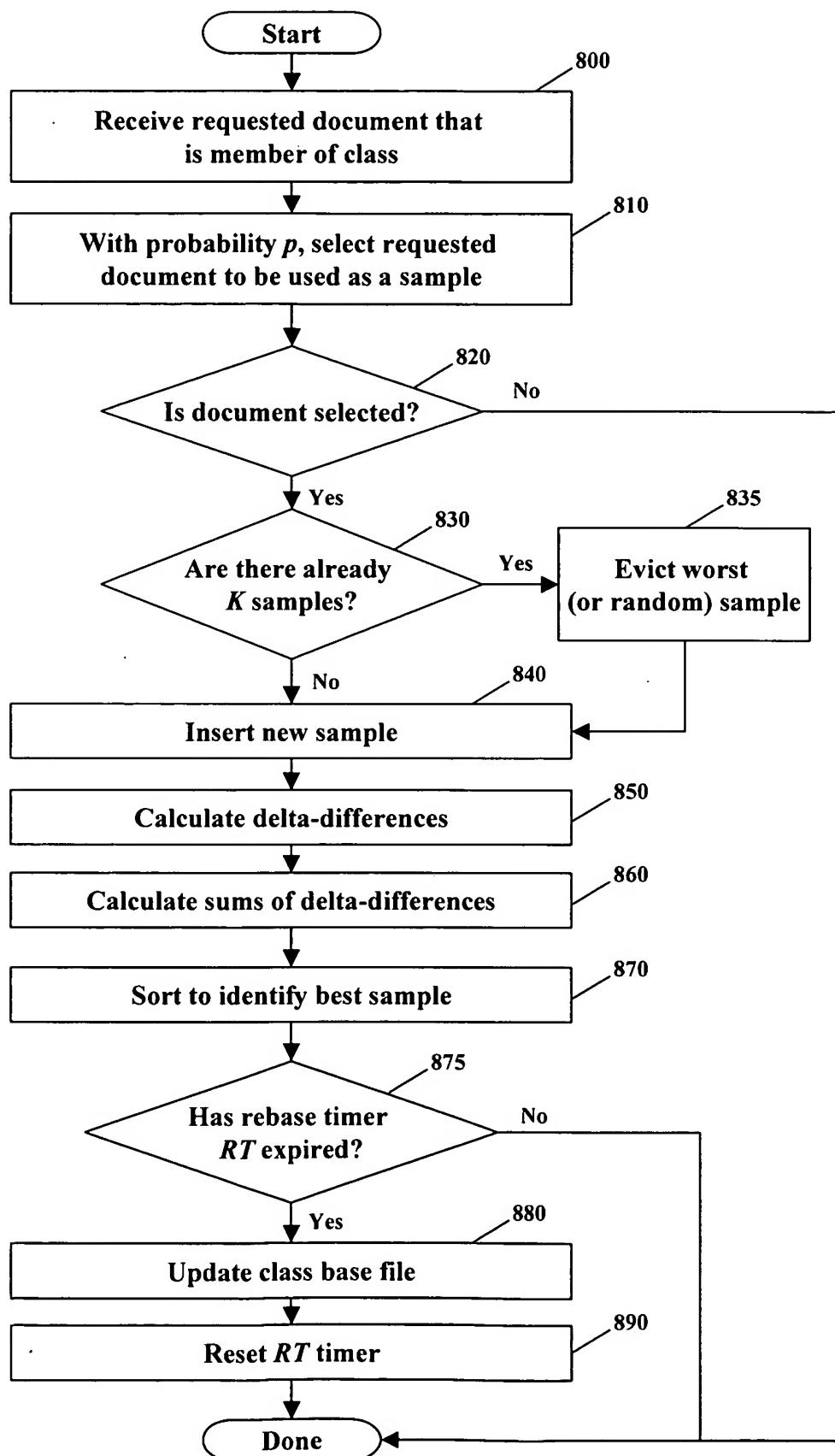


FIGURE 8

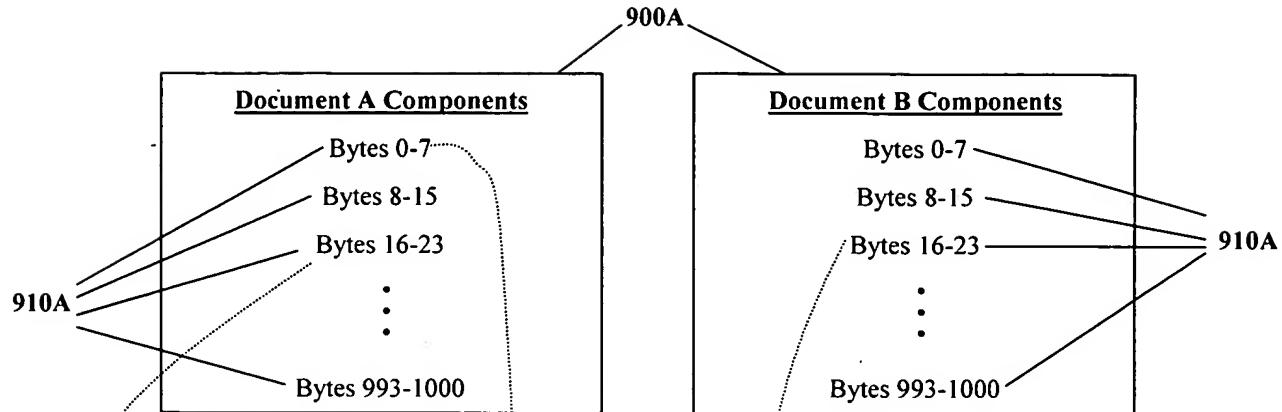


FIGURE 9A

COMPONENTS	COUNTER	ACTIVE/INACTIVE BIT
Component A	12	Active
Component B	15	Active
Component C	10	Active
⋮	⋮	⋮
Component Y	1	Inactive
Component Z	0	Inactive

900B

910B

920B

930B

FIGURE 9B

□□□□□□□□□□

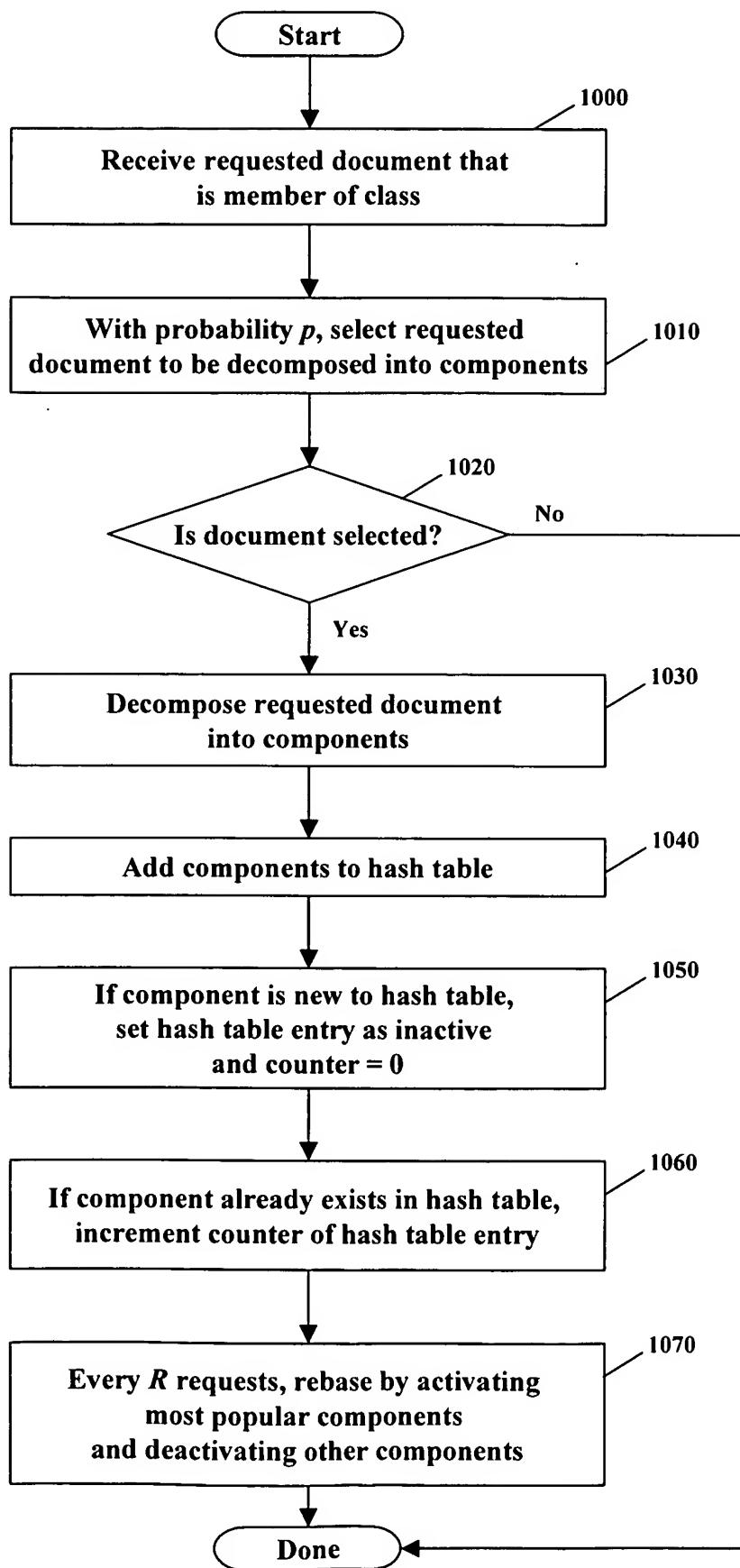


FIGURE 10

0
0
4
5
6
0
0
2
3
0
0
0
0
0

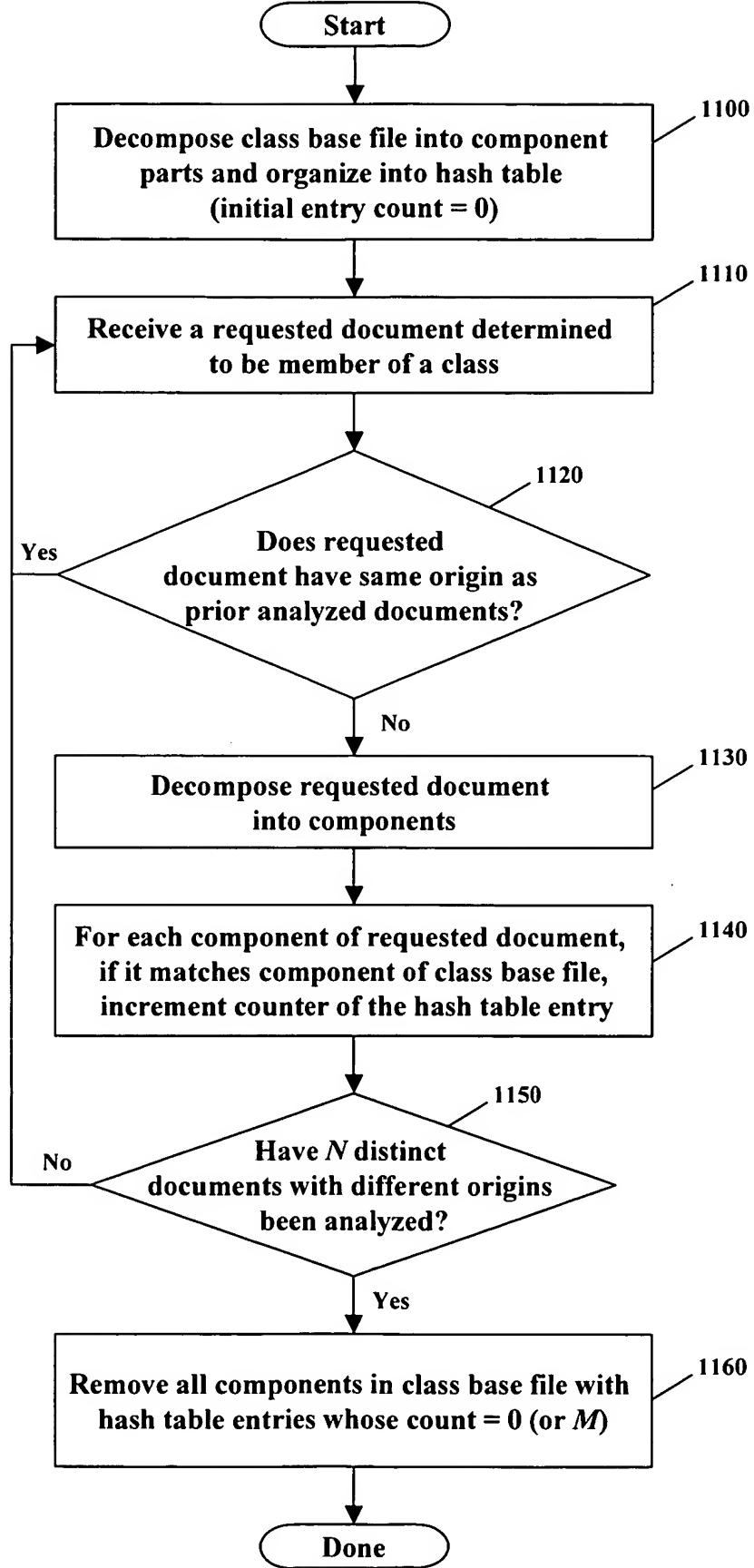


FIGURE 11